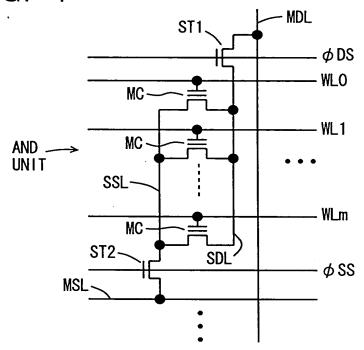
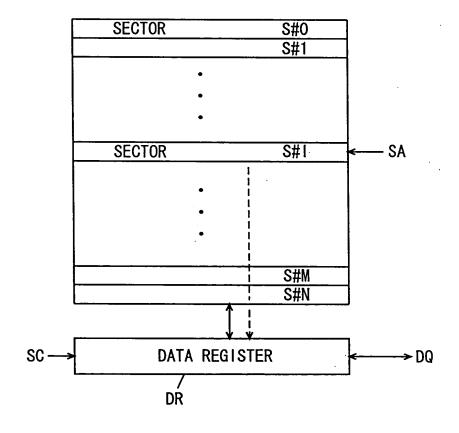
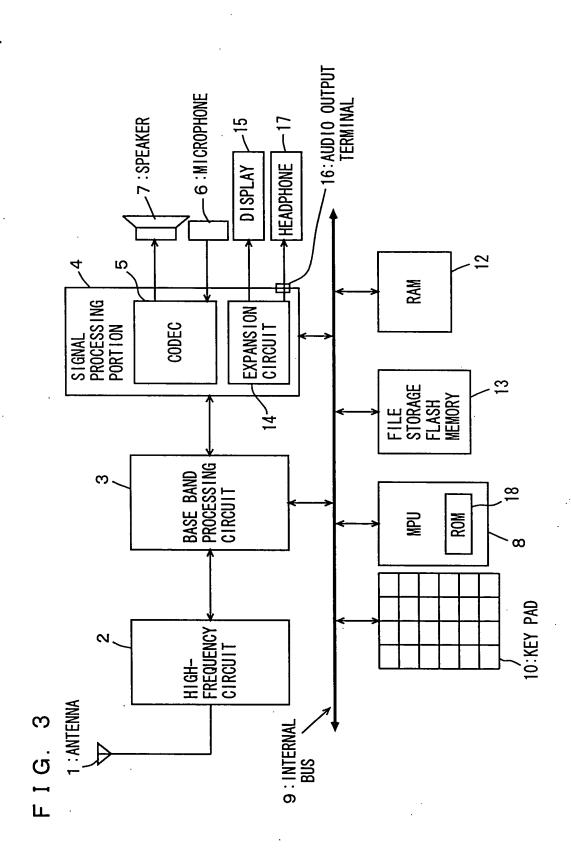
F I G. 1

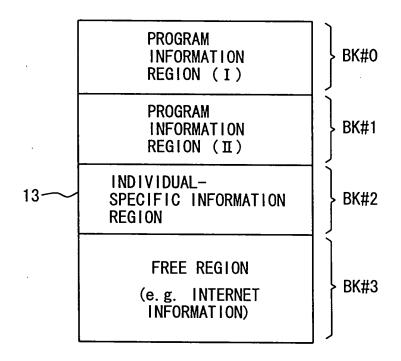


F I G. 2

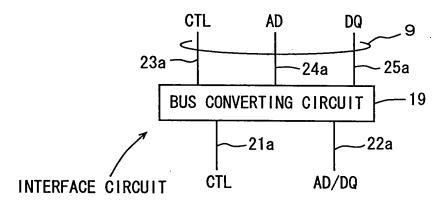




F I G. 4



F I G. 8



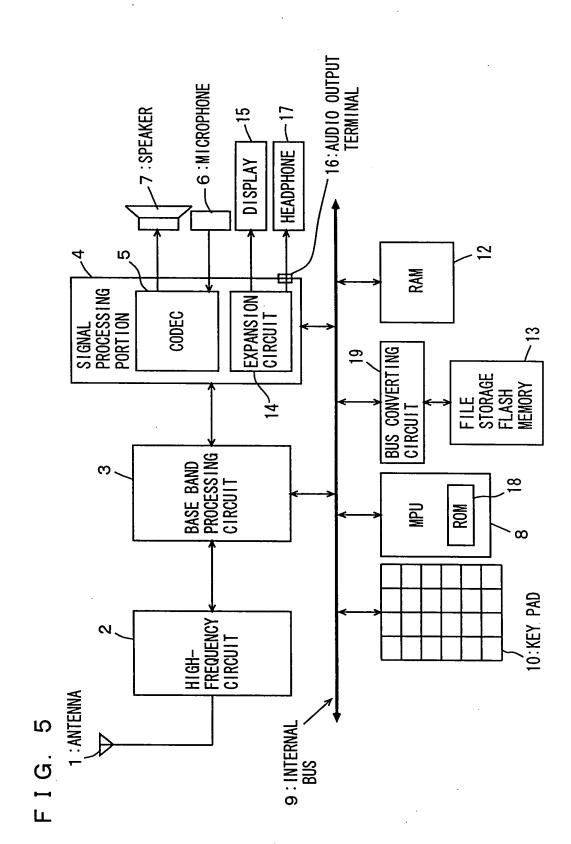


FIG. 6A

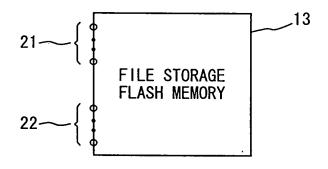
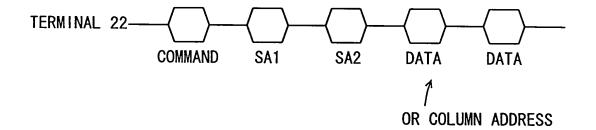
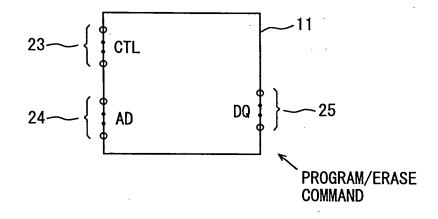


FIG. 6B



F I G. 7



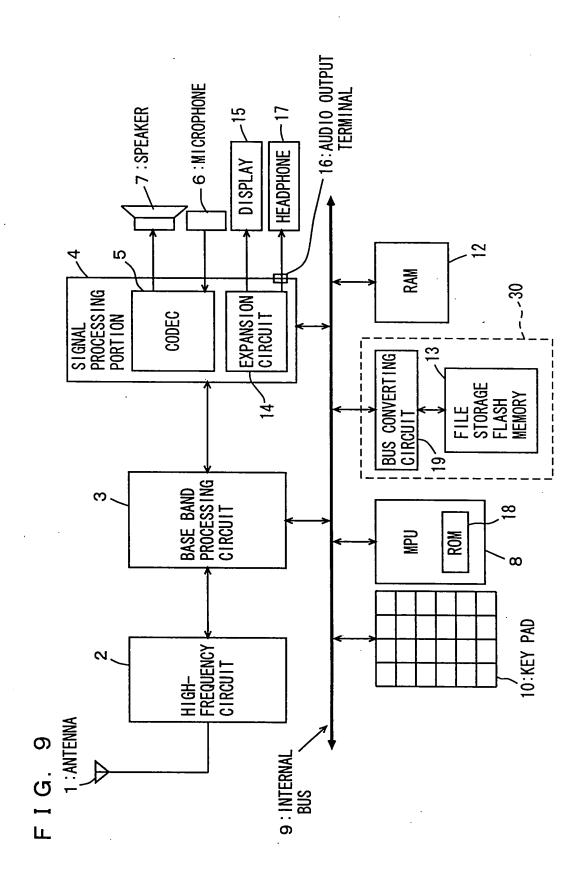


FIG. 10

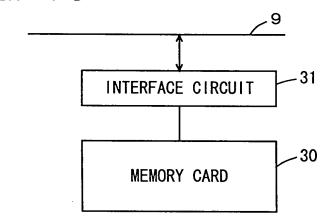


FIG. 11

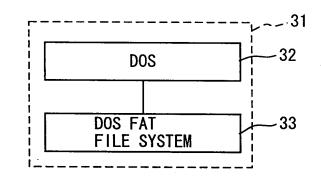
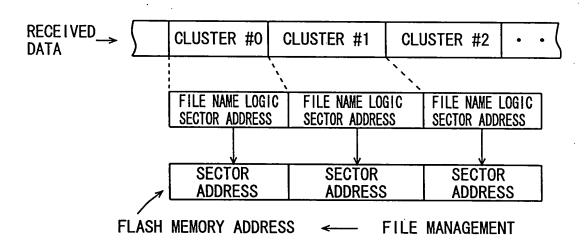
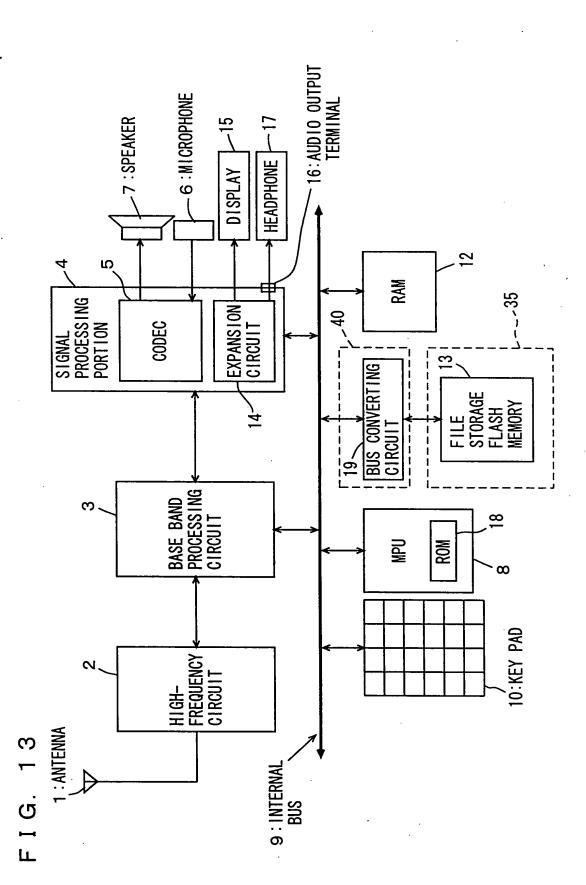
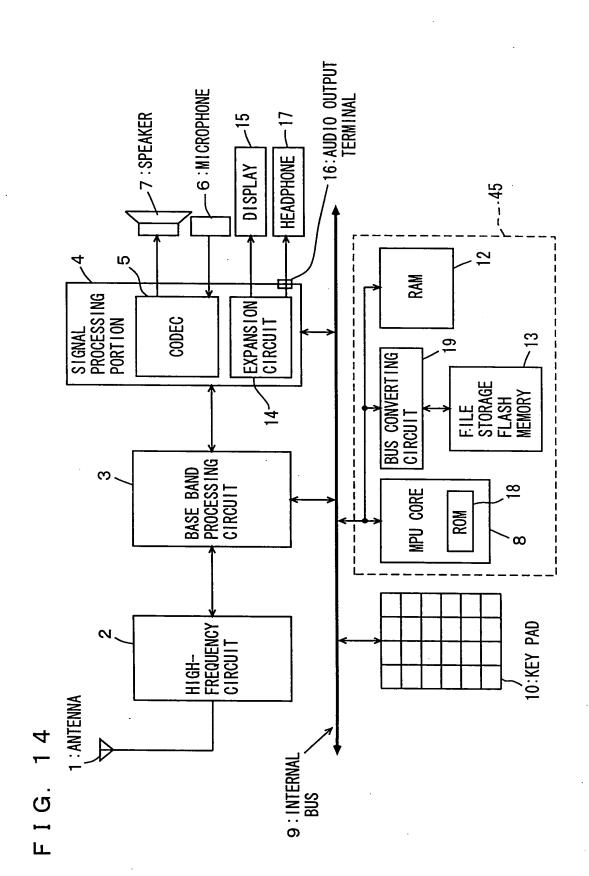


FIG. 12







- 6: MICROPHONE സ് RAM SIGNAL PROCESSING PORTION CODEC LINEAR FLASH MEMORY BASE BAND PROCESSING CIRCUIT MPU ∞ 10:KEY PAD HIGH-FREQUENCY FIG. 15 PRIOR ART 1 : ANTENNA 9:INTERNAL BUS

7 : SPEAKER

FIG. 16 PRIOR ART

